

What is claimed is:

50B7 1. A display apparatus having a plurality of pixels, comprising
on a substrate:

5 a plurality of pixel electrodes corresponding to
respective pixels among the plurality of pixels,

a plurality of thin film transistors, each comprising a
plurality of conductive layers, for controlling supplying of
signal voltage to the plurality of pixel electrodes,

10 a plurality of input terminals for receiving a control
signal for the signal voltage to be supplied to the plurality of
thin film transistors;

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15 wires for sending the signal voltage from the plurality
of input terminals to the plurality of thin film transistors, at
least a portion thereof having a lamination structure comprising
two or more conductive layers corresponding to a plurality of
conductive layers constituting each thin film transistor.

20 2. An apparatus according to claim 1, wherein the portion of
the wires having a conductive layer correspond to the lowest
conductive layer included in each thin film transistor.

50B2 3. A display apparatus having a plurality of pixels, comprising
on a substrate:

25 a plurality of pixel electrodes corresponding to
respective pixels among the plurality of pixels,

a plurality of switching thin film transistors, each
comprising a plurality of conductive layers, connected to the

plurality of pixel electrodes, for supplying signal voltage to the plurality of pixel electrodes;

5 a plurality of driving thin film transistors, each comprising a plurality of conductive layers, arranged close to peripheral area of the plurality of pixel electrodes, for generating a driving signal for driving the number of switching thin film transistors;

10 a plurality of input terminals for receiving a control signal for driving the plurality of driving thin film transistors; and

15 wires for connecting the plurality of driving thin film transistors and the plurality of input terminals, at least a portion thereof having a lamination structure comprising two or more conductive layers similar to the plurality of conductive layers included in each switching thin film transistor and/or each driving thin film transistor.

4. An apparatus according to claim 3, wherein

20 At least a portion of the wire has a conductive layer similar to the lowest conductive layer included in each switching thin film transistor and/or each driving thin film transistor.

5. An apparatus according to claim 3, wherein a portion of the wire is arranged outside at least a part of the plurality of driving thin film transistors.

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6. A display apparatus having a plurality of pixels, comprising on a substrate:

a plurality of pixel electrodes corresponding to respective pixels among the plurality of pixels,

a plurality of switching thin film transistors, each comprising a plurality of conductive layers, connected to the plurality of pixel electrodes, for supplying signal voltage to the plurality of pixel electrodes;

a plurality of driving thin film transistors, each comprising a plurality of conductive layers, arranged close to peripheral area of the plurality of pixel electrodes, for generating a driving signal for driving the number of switching thin film transistors;

wires for connecting the plurality of driving thin film transistors and the plurality of input terminals; and

a plurality of input terminals for receiving a control signal for driving the plurality of driving thin film transistors, having a lamination structure comprising two or more conductive layers similar to the plurality of conductive layers included in each switching thin film transistor and/or each driving thin film transistor, and situated 0.8 mm or further from the plurality of driving thin film transistors.

7. A mother substrate for making a display apparatus; comprising:

a plurality of active matrix substrates including a plurality of pixel electrodes each corresponding to each of the plurality of pixels,

a plurality of switching thin film transistors, each comprising a plurality of conductive layers, connected to the

plurality of pixel electrodes, for supplying signal voltage to the plurality of pixel electrodes,

a plurality of driving thin film transistors, each comprising a plurality of conductive layers, arranged close the
5 plurality of pixel electrodes for generating a driving signal for driving the number of switching thin film transistors,

a plurality of input terminals for receiving a control signal for driving the plurality of driving thin film transistors, and

10 wires for connecting the plurality of driving thin film transistors and the plurality of input terminals; and

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a region where a discharge conductive section is formed between adjacent active matrix substrates, the discharge conductive section having a conductive layer similar to the
15 lowest conductive layer included in each switching thin film transistor and/or each driving thin film transistor.

8. A mother substrate according to claim 7, wherein the region where a discharge conductive section is formed is discarded when
20 the plurality of active matrix substrates are separated.

9. A display apparatus having a plurality of pixels, comprising:

a plurality of thin film transistors on a substrate, each
25 comprising a plurality of conductive layers, for controlling display via the plurality of pixels; and

a discharge conductive section on the substrate, having a lamination structure comprising at least two conductive layers

similar to the plurality of conductive layers constituting each thin film transistor.

10. A display apparatus according to claim 9, wherein the
5 discharge conductive section constitutes at least a portion of a wire for sending a signal for controlling the plurality of thin film transistors.

11. A display apparatus according to claim 9, wherein the
10 discharge conductive section is provided electrically independent from the other conductive sections.

12. A display apparatus according to claim 9, wherein the
15 discharge conductive section is provided outside the plurality of thin film transistors.

13. A method for manufacturing a display apparatus; wherein
the display apparatus comprises

20 a plurality of thin film transistors on a substrate,
each comprising a plurality of conductive layers for controlling display via the plurality of pixels; and

a discharge conductive section carried on the substrate,
having a lamination structure comprising at least two conductive
layers similar to a plurality of conductive layers constituting
25 each thin film transistor, and

wherein

the plurality of conductive layers of the plurality of thin film transistors and the discharge conductive section are formed

during the same manufacturing step.

14. A method for manufacturing a display apparatus, comprising:
a step of making a mother substrate having a plurality of

5 active matrix substrates each including

a plurality of pixel electrodes corresponding to the
plurality of pixels,

a plurality of switching thin film transistors, each
comprising a plurality of conductive layers, connected to the
10 plurality of pixel electrodes, for supplying signal voltage to
the plurality of pixel electrodes,

a plurality of driving thin film transistors each
comprising a plurality of conductive layers arranged close to the
plurality of pixel electrodes, for driving the number of
15 switching thin film transistors,

a plurality of input terminals for receiving a control
signal for driving the plurality of driving thin film transistors,

wires for connecting the plurality of driving thin film
transistors and the plurality of input terminals, and

20 a region where a discharge conductive section is formed
between adjacent active matrix substrates, said discharge
conductive section having a conductive layer similar to the
lowest conductive layer constituting each switching thin film
transistor and/or each driving thin film transistor; and

25 a step of separating the plurality of active matrix
substrates from the mother substrate while discarding the region
where the discharge conductive section is formed whereby a
plurality of display apparatuses are manufactured.